EXHIBIT 1

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UNITED STATES DISTRICT COURT NORTHERN DISTRICT OF CALIFORNIA SAN FRANCISCO DIVISION

WAYMO LLC,

Plaintiff,

vs. Case No.

UBER TECHNOLOGIES, INC.; 3:17-cv-00939-WHA

OTTOMOTTO LLC; OTTO TRUCKING LLC,

Defendants.

HIGHLY CONFIDENTIAL - ATTORNEYS' EYES ONLY

VIDEOTAPED DEPOSITION OF ANDREW WOLFE, Ph.D. FRIDAY, AUGUST 11, 2017

Reported by:

Anrae Wimberley

CSR No. 7778

Job No. 2678828

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1	qualitatively or generally quantitatively what	10:36:16
2	happens. Without numbers on it, you can't	10:36:19
3	really you can get ratios between things, but	10:36:24
4	they're arbitrary points on the curve. So I don't	10:36:30
5	know what that would mean to anybody.	10:36:32
6	BY MS. YANG:	10:36:32
7	Q. As shown in your own figure, when you say the	10:36:34
8	current flow is restricted in paragraph 52, how much	10:36:40
9	flow is getting through the diode in the example of	10:36:45
10	paragraph 52?	10:36:46
11	A. It would vary from diode to diode, but it's	10:36:50
12	generally several orders of magnitude less than the	10:36:53
13	maximum forward current of the diode.	10:36:56
14	Q. So going to Column 18 of the patent, at lines	10:37:54
15	62 to 67 and this is Figure 5B in your own	10:38:12
16	declaration, matching up against this. So let's just	10:38:15
17	walk through that again.	10:38:16
18	MS. YANG: I'm looking at Figure so just to	10:38:22
19	clear up for the record, I'm looking at Columns 18,	10:38:26
20	lines 62 to 67, of the '936 patent, which is Exhibit	10:38:31
21	1425. And I'm looking at Figure 5B of the patent,	10:38:35
22	which is replicated in paragraph 65 of Dr. Wolfe's	10:38:39
23	declaration on page 22.	10:38:41
24	THE WITNESS: I'm sorry. You wanted me to look at	10:38:44
25	paragraph okay. Okay.	10:38:51

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1	BY MS. YANG:	10:38:51
2	Q. The specification states, "Upon the diode 514	10:38:53
3	being reverse biased, the current through the	10:38:56
4	inductor 510 goes to zero and the voltage across the	10:39:01
5	inductor 510 settles at zero, which sets node A to the	10:39:07
6	voltage of the voltage source 502 (e.g., the voltage	10:39:11
7	V1), but the capacitor may hold a higher voltage	10:39:16
8	(e.g., about 2 V1)."	10:39:17
9	So matching that up against the curves shown	10:39:22
10	in Figure 5B, when the diode is reversed by T2, the	10:39:28
11	current through inductor 510, which is shown by the	10:39:32
12	curve IInd, it goes to zero; is that correct?	10:39:36
13	A. For this particular embodiment in this	10:39:38
14	particular example, that's true.	10:39:40
15	Q. And so in the '936 patent, it's describing	10:39:44
16	the '936 patent is describing an idealized diode, is	10:39:50
17	that correct, where there's no reverse current	10:39:54
18	described in the Figure 5B or in the specification; is	10:39:59
19	that true?	10:40:01
20	MR. NEWTON: Objection; form.	10:40:39
21	THE WITNESS: It's not clear. I guess	10:40:50
22	theoretically it's idealized, but what it's really	10:40:55
23	showing here is that the voltage at node A and the	10:40:58
24	voltage at the other side of the diode at this	10:41:02
25	particular period of time are so close, that there's	10:41:10

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1	very, very little difference between them.	10:41:12
2	So when it tells us that it's reverse	10:41:15
3	biased if, for example, we were to look at the	10:41:22
4	figure that I have on page 18, it's telling us here	10:41:28
5	that it's just a tiny bit to the left of the axis line	10:41:34
6	into the blue region.	10:41:38
7	So if we were to be hypertechnical about it,	10:41:40
8	there would be a very, very small reverse current, but	10:41:51
9	it's right up against the point where it would be	10:41:54
10	zero. So	10:41:57
11	BY MS. YANG:	
12	Q. Well	10:41:58
13	A. The words "reverse biased" tell us that it	10:42:01
14	exists, but we're right up against the null point in	10:42:06
15	that particular situation that's being described	10:42:09
16	there. So the current is about close to zero as	10:42:12
17	anybody would care about.	10:42:14
18	Q. And earlier today, an hour ago, we talked	10:42:18
19	about the curve IInd in Figure 5B. And at least as	10:42:22
20	illustrated in Figure 5B, the curve at T2 goes to zero	10:42:28
21	and stays at zero until at least about the point TRx	10:42:33
22	in this figure; is that correct?	10:42:36
23	MR. NEWTON: Objection; form.	10:42:50
24	THE WITNESS: Yeah, at least close enough to zero	10:42:52
25	that nobody would care about it. It would not stay at	10:42:55

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1	Exhibit 1428.	11:23:37
2	THE REPORTER: Our next exhibit is 1429.	11:24:02
3	(Defendants' Exhibit 1429 was marked.)	11:24:20
4	MS. YANG: Can we go off the record.	11:24:21
5	THE VIDEOGRAPHER: We are off the record at 11:24	11:24:23
6	a.m.	11:24:23
7	(Recess taken.)	11:37:45
8	THE VIDEOGRAPHER: We are back on the record at	11:40:32
9	11:40 a.m.	11:40:34
10	BY MS. YANG:	11:40:34
11	Q. I need to do a little bit of just recapping.	11:40:40
12	So if we can go back to Figure 5A of the patent as	11:40:45
13	well as Claim 1. And maybe the best way to do that is	11:40:49
14	to look at Claim 1 of the patent, which is	11:40:52
15	A. I have two copies, so I'm fine.	11:40:54
16	Q. You have it. Okay. Fantastic.	11:40:56
17	Just to clarify for the record, Claim 1 of	11:40:59
18	the patent, which is Exhibit 1425. And also looking	11:41:03
19	at Figure 5 at the same time. And I too have two	11:41:09
20	copies of the patent.	11:41:12
21	What is the charging path of the '936 patent,	11:41:27
22	in your opinion?	11:41:28
23	MR. NEWTON: I'll object. This is outside the	11:41:32
24	scope. Dr. Wolfe didn't offer an opinion on this	11:41:35
25	term. These questions are improper.	11:41:38

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1	BY MS. YANG:	11:41:38
2	Q. Is there any reason you didn't offer an	11:41:42
3	opinion on the charging path claim limitation?	11:41:45
4	A. Counsel never asked me to.	11:41:48
5	Q. Understood.	11:41:48
6	Just so just to recap then, the	11:42:05
7	claim earlier today we talked about the inductor	11:42:08
8	being 510, element 510 in circuit 5A; is that correct?	11:42:14
9	A. In that one particular example, element 510	11:42:19
10	is an inductor.	11:42:20
11	Q. And then there is a diode coupled to the	11:42:25
12	voltage source via the inductor.	11:42:27
13	So the diode is 514; is that correct?	11:42:30
14	A. In that example embodiment, 514 would be an	11:42:36
15	example of a diode that could meet that claim language	11:42:39
16	in Claim 1.	11:42:40
17	Q. When you say via the inductor, does the claim	11:42:45
18	require the inductor 510 to be between 514 and 502?	11:42:52
19	MR. NEWTON: Same objection. It's outside the	11:42:53
20	scope of Dr. Wolfe's claim construction opinions.	11:42:56
21	THE WITNESS: Can I hear the question again.	11:42:59
22	BY MS. YANG:	11:42:59
23	Q. I'm just asking what the charging path is	11:43:01
24	here.	11:43:01
25	So is the inductor when the claim language	11:43:06

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1	here.	12:24:30
2	When you describe in your declaration,	12:24:36
3	paragraph 44, that "the diode 414 becomes reverse	12:24:39
4	biased to block the current flow," you mean that in	12:24:42
5	sort of the ideal physics law of physics	12:24:45
6	theoretical sense, correct, that the flow is blocked,	12:24:49
7	obviously there's some small orders of magnitude	12:24:53
8	smaller reverse current, but the flow overall is	12:24:58
9	blocked at when the diode is reverse biased; is	12:25:01
10	that correct?	12:25:03
11	A. No. I mean what I explained before. Because	12:25:03
12	the voltage is very low that, for a practical sense,	12:25:08
13	it's blocked. That's the patentee's language, not	12:25:11
14	mine. I'm just repeating it. But that's what	12:25:15
15	"blocked" means in that sense, is that it's reverse	12:25:17
16	biased because we're near the zero point and there's	12:25:22
17	little or no current that's flowing.	12:25:25
18	Q. Finally, returning to paragraph 52 of	12:25:41
19	your turning to paragraph 52 of your declaration,	12:25:46
20	just to be clear, you use the words "the diode becomes	12:25:50
21	'reversed biased' and restricts the current flow in	12:25:53
22	the opposite direction."	12:25:54
23	You don't use the word "resists" the current	12:25:58
24	flow; is that correct?	12:25:58
25	A. It's true that I didn't use that word, but as	12:26:01

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1 2	FEDERAL CERTIFICATE OF DEPOSITION OFFICER
	I, ANRAE WIMBERLEY, CSR NO. 7778, do hereby
3	declare:
4	That, prior to being examined, the witness named
	in the foregoing deposition was by me duly sworn
5	pursuant to Section 30(f)(1) of the Federal Rules of
	Civil Procedure and the deposition is a true record of
6	the testimony given by the witness;
7	That said deposition was taken down by me in
	shorthand at the time and place therein named and
8	thereafter reduced to text under my direction;
9	That the witness was requested to
	review the transcript and make any changes to the
LO	transcript as a result of that review pursuant to
L1	Section 30(e) of the Federal Rules of Civil Procedure;
	No changes have been provided by the
12	witness during the period allowed;
L3	The changes made by the witness are
14	appended to the transcript;
	No request was made that the transcript
15	be reviewed pursuant to Section 30(e) of the Federal
16	Rules of Civil Procedure.
	I further declare that I have no interest in the
17	event of the action.
18	I declare under penalty of perjury under the laws
	of the United States of America that the foregoing is
19	true and correct.
20	WITNESS my hand this 14th day of August, 2017.
21	
22	Anna Williamberley
23	
24	AND A E MIMDEDIEV CCD NO. 7779
25	ANRAE WIMBERLEY, CSR NO. 7778